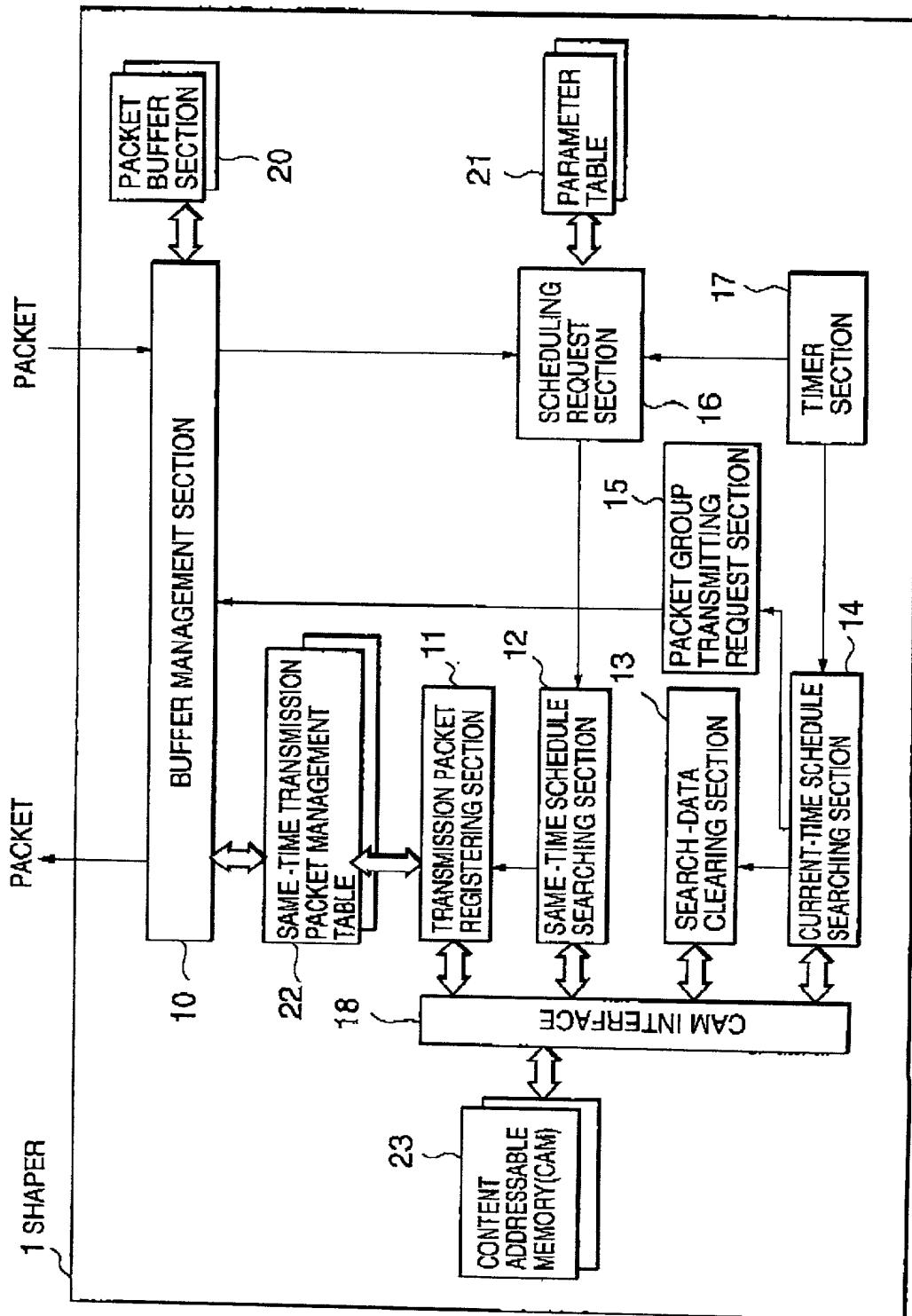


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FIG. 1



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FIG.2

CAM

VALID/INVALID BIT	SCHEDULE TIME
0	
1	
2	
3	
4	
5	
...	...
254	
255	

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FIG.31ST LINK TABLE OF SAME-TIME TRANSMISSION
PACKET MANAGEMENT TABLE

	EMPTY	TOP	TAIL
0			
1			
2			
3			
4			
5			
⋮	⋮	⋮	⋮
254			
255			

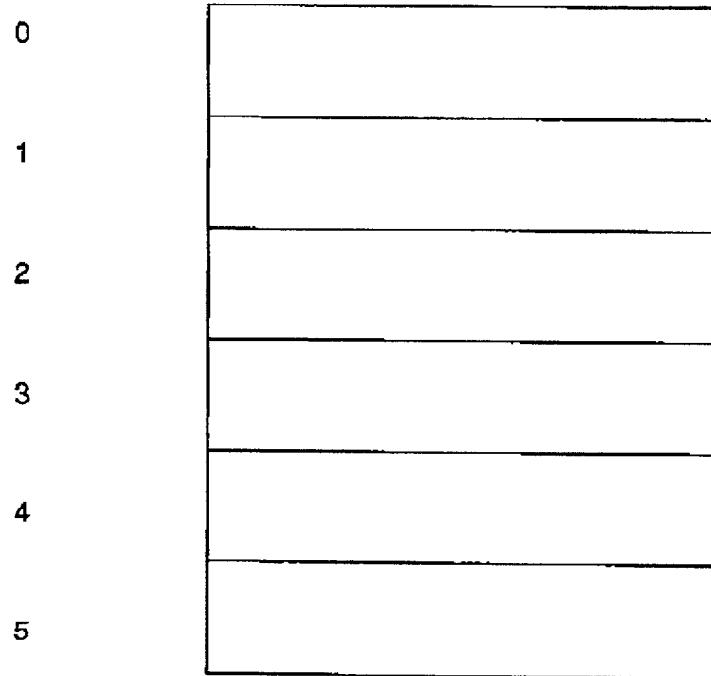
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FIG.4

2ND LINK TABLE OF SAME-TIME TRANSMISSION
PACKET MANAGEMENT TABLE

NEXT



254

255

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FIG.5

QEMPTY	QTOP	QTAIL
0		
1		
2		
3		
4		
5		
...		...
254		
255		

FIG.6

QNEXT
0
1
2
3
4
5
6
7
...
n
n-1

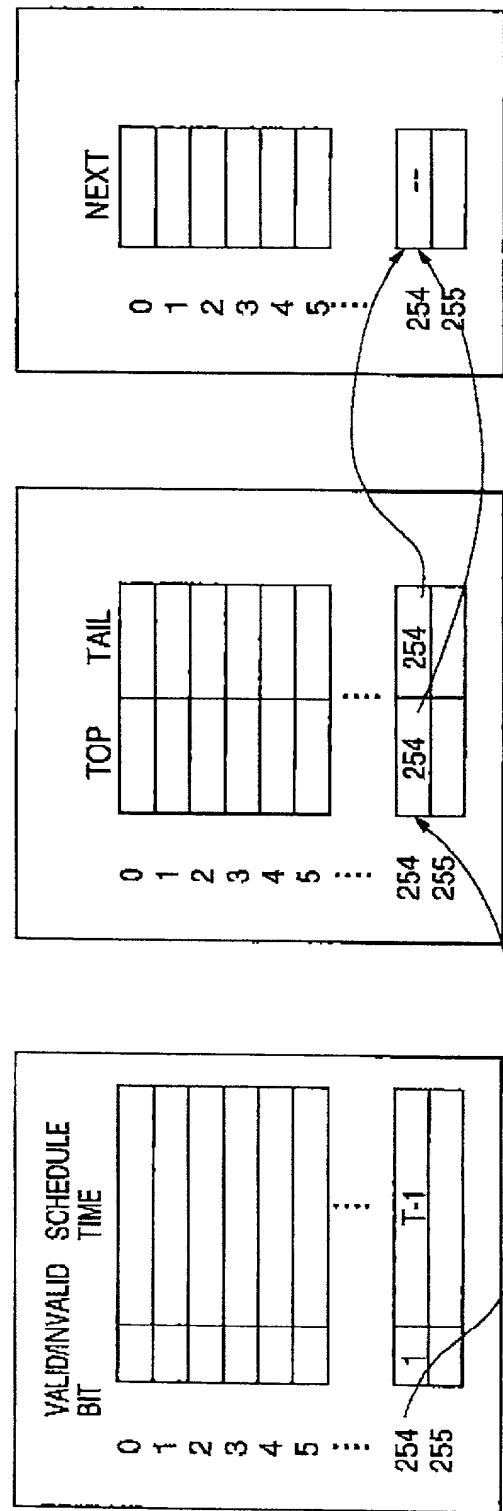
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FIG. 7

TABLE FOR MANAGING TOP POINTER AND TAIL POINTER OF SAME-TIME LIST STRUCTURE

TABLE

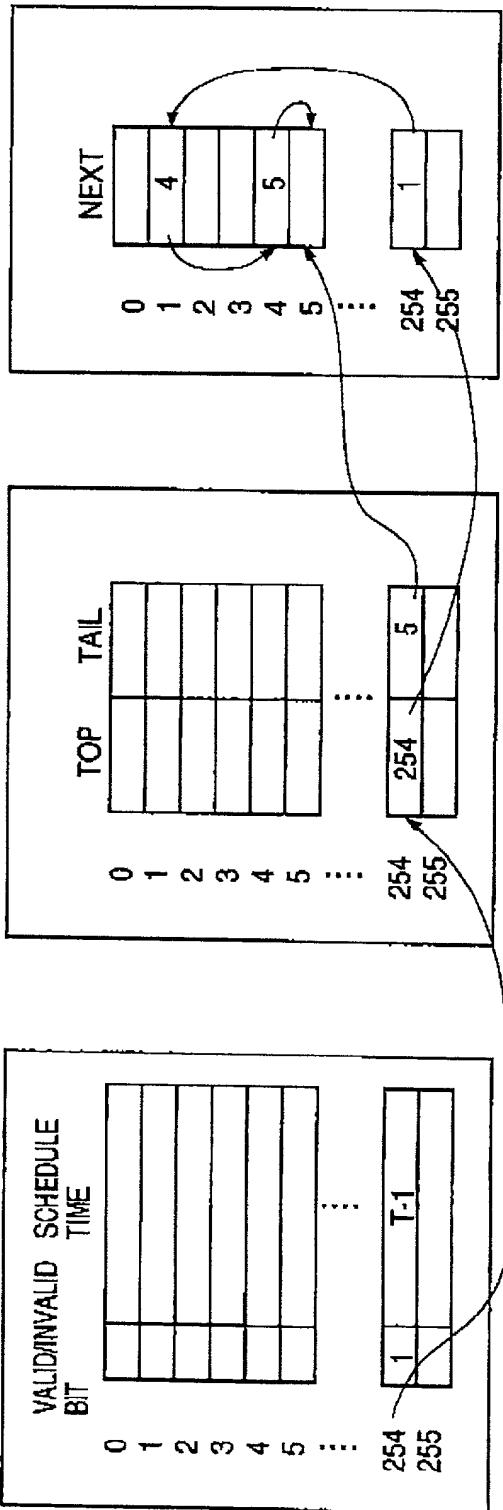


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FIG.8

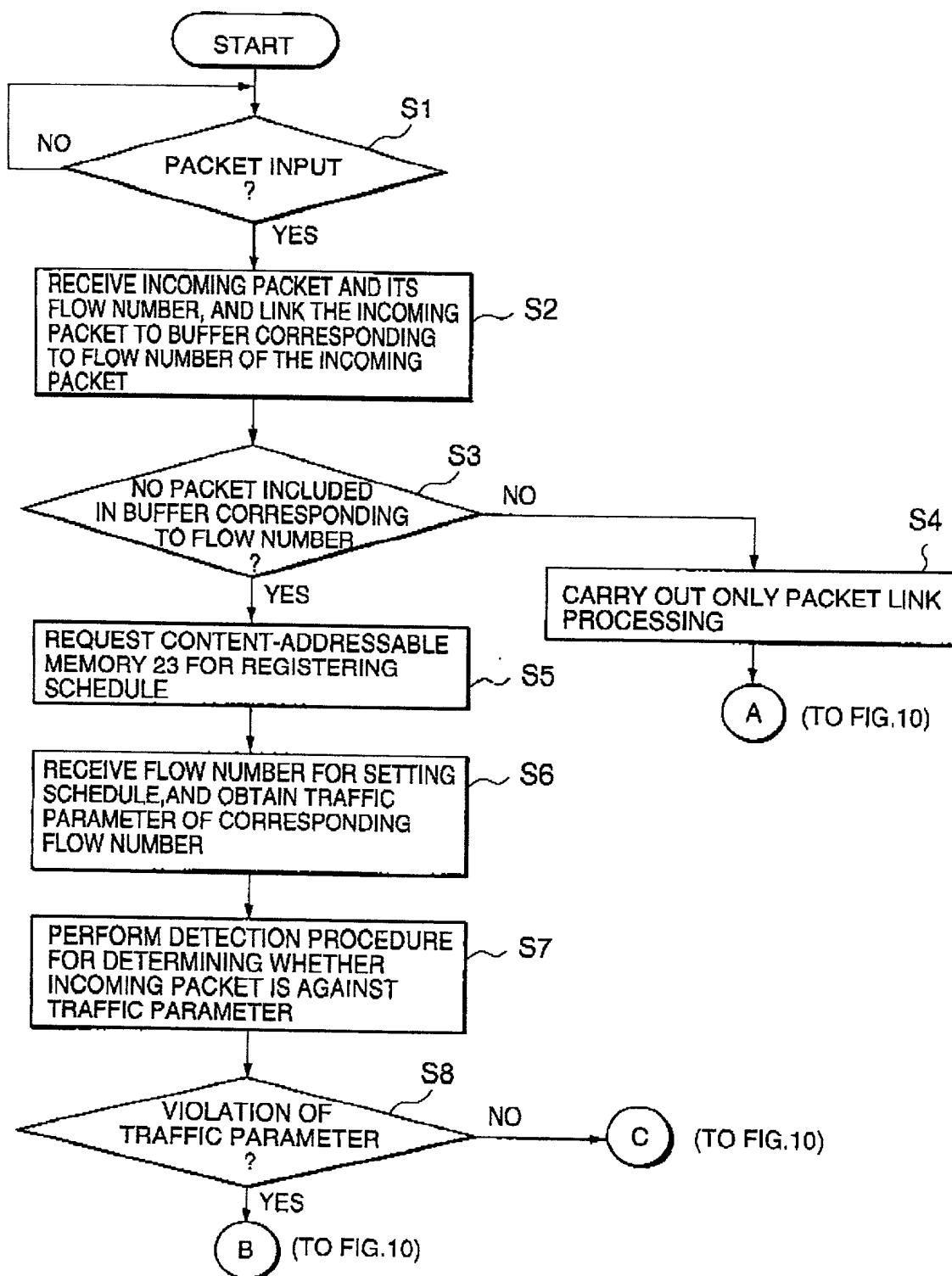
TABLE FOR MANAGING SAME-TIME LIST STRUCTURE



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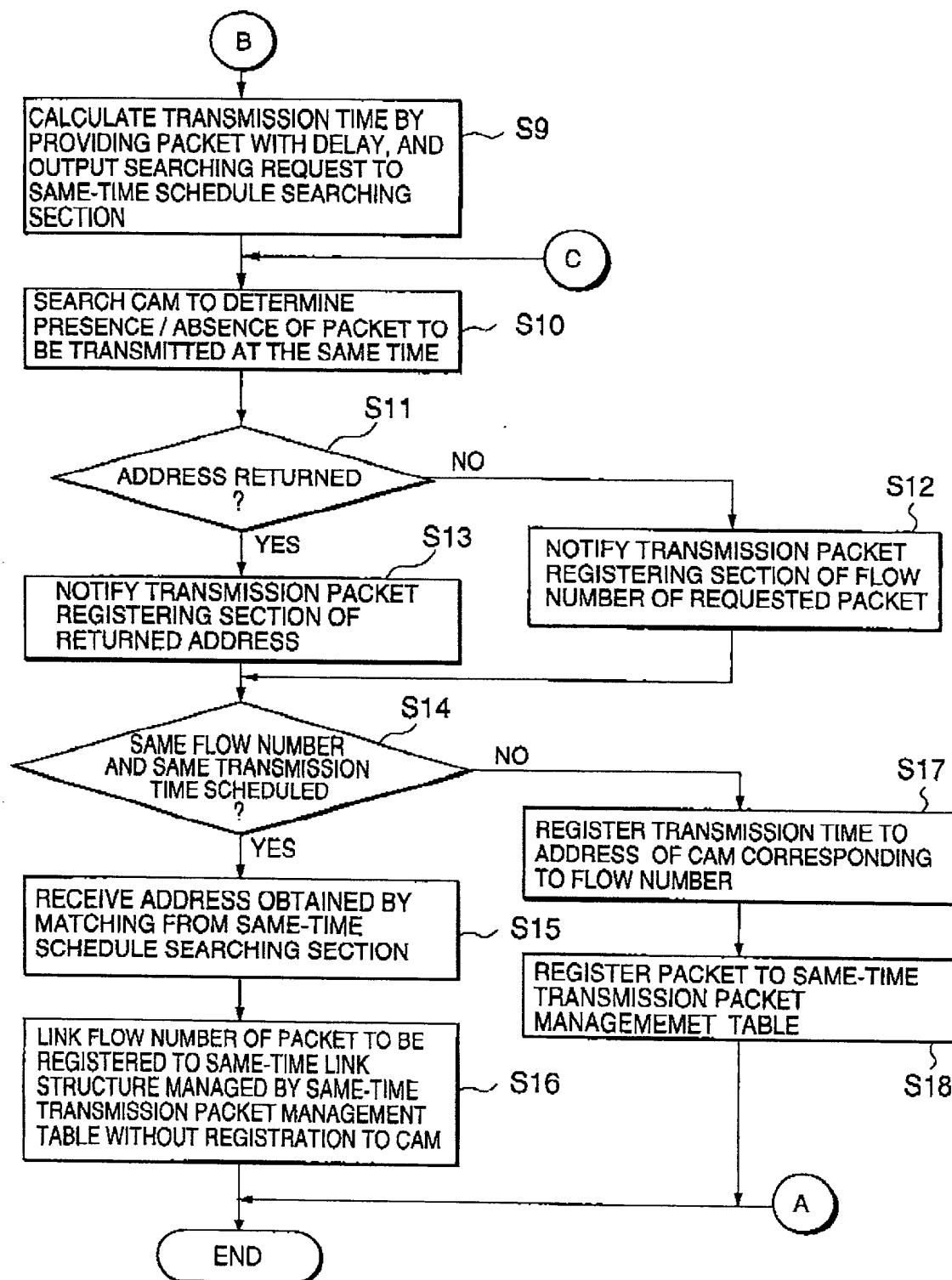
FIG.9



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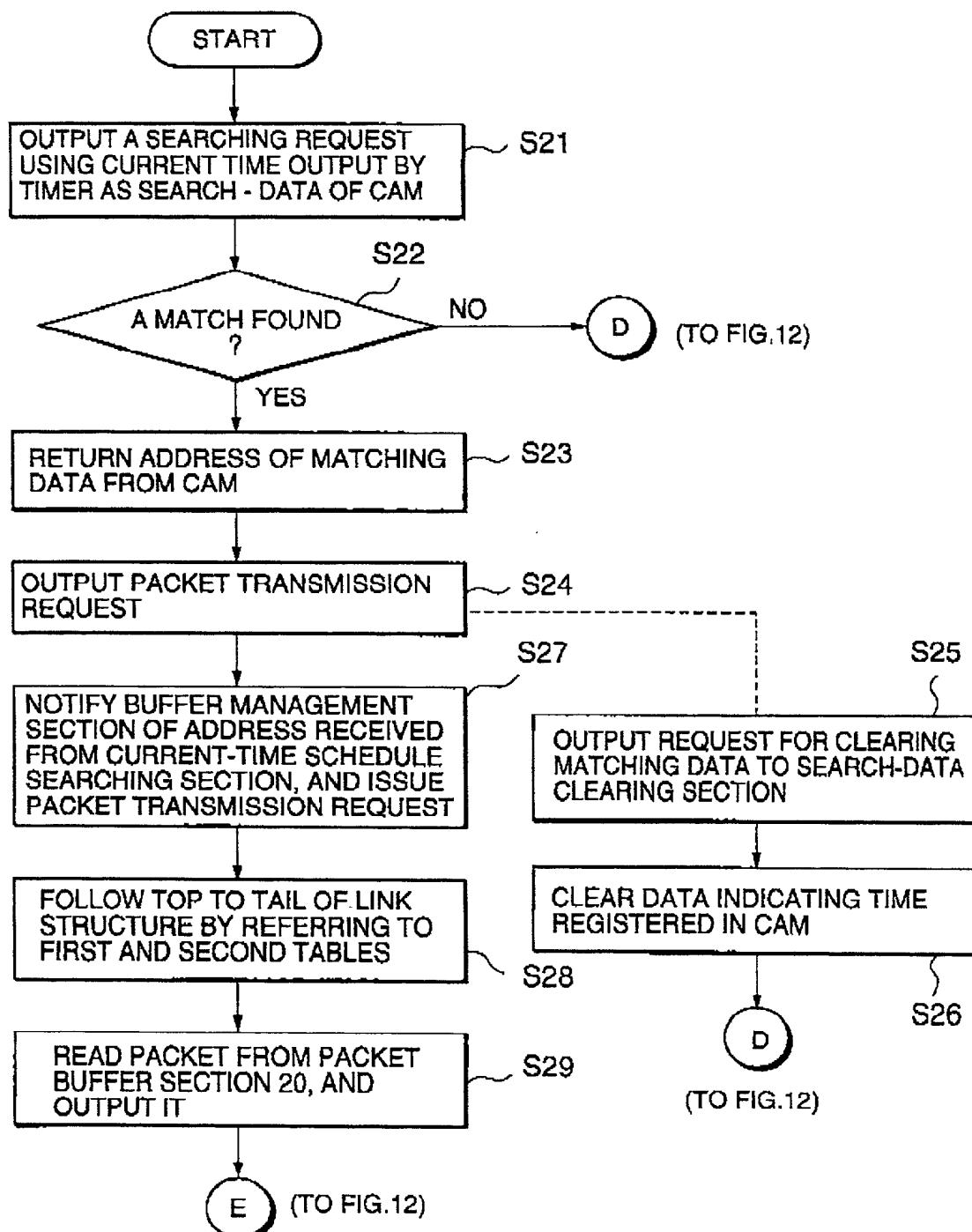
FIG.10



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FIG.11



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FIG.12

